

FIG. 1

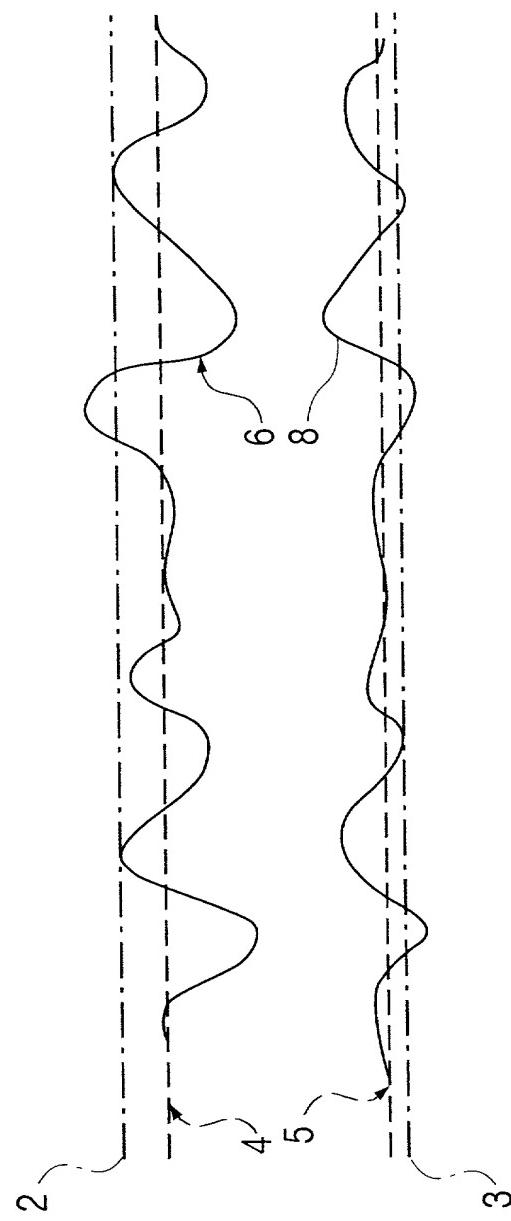


FIG. 2

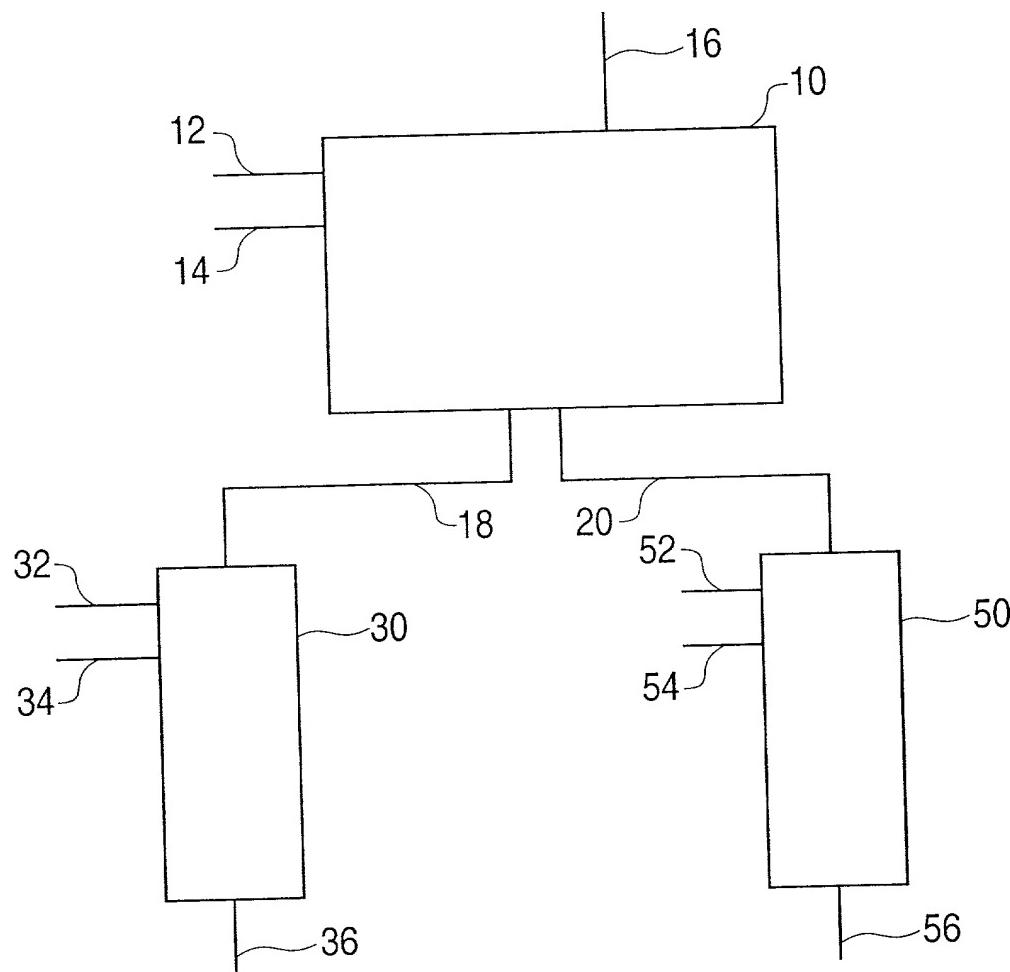


FIG. 3

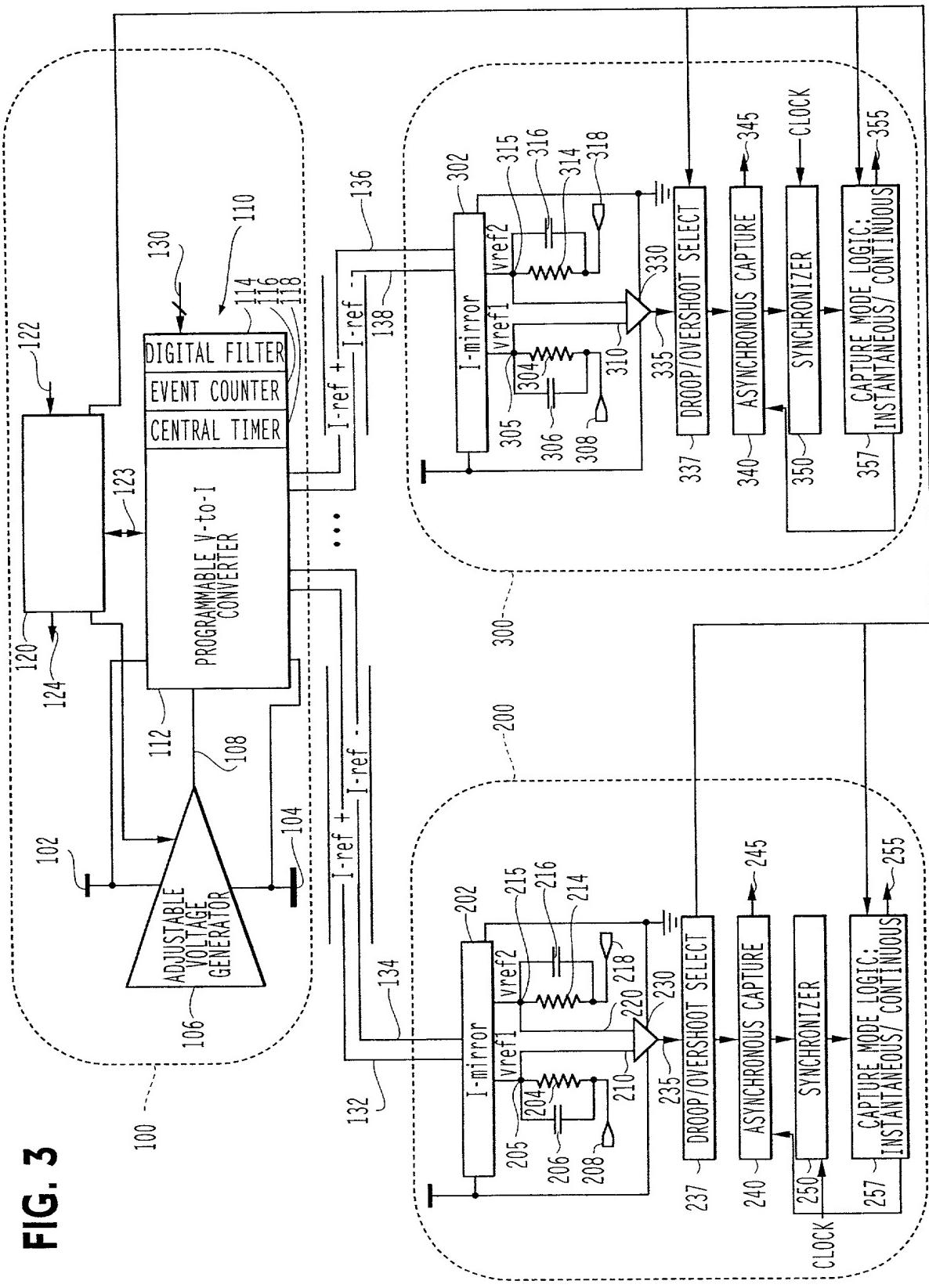


FIG. 4

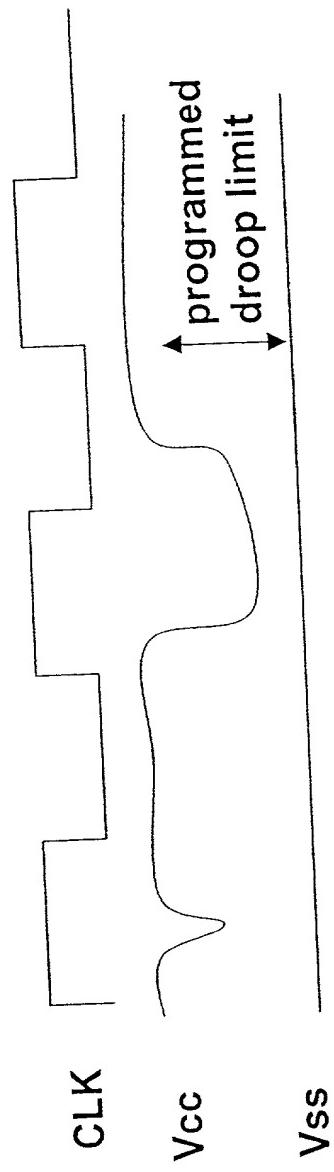


FIG. 5

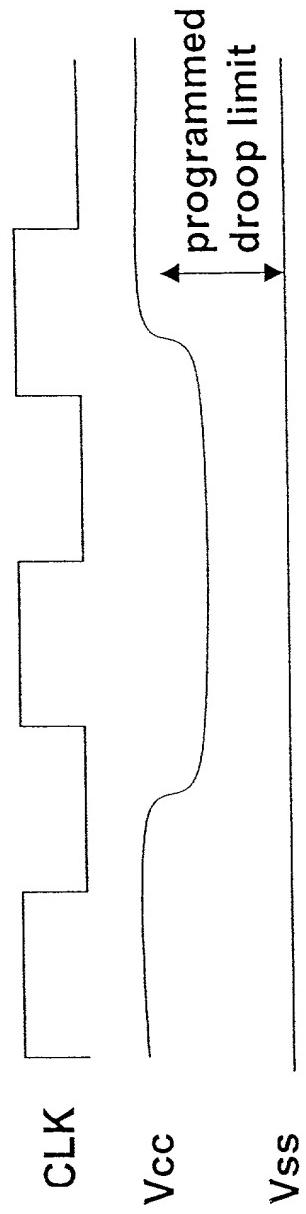


FIG. 6

CLK _____

(core clock does not need to be ON)

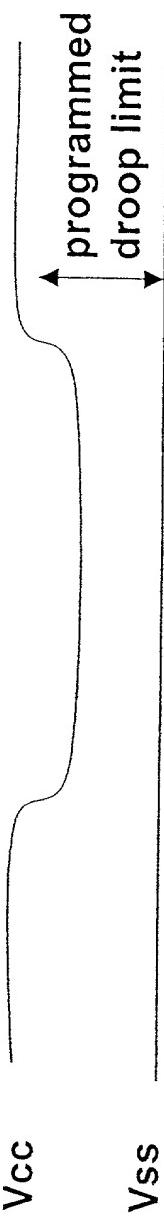


FIG. 7

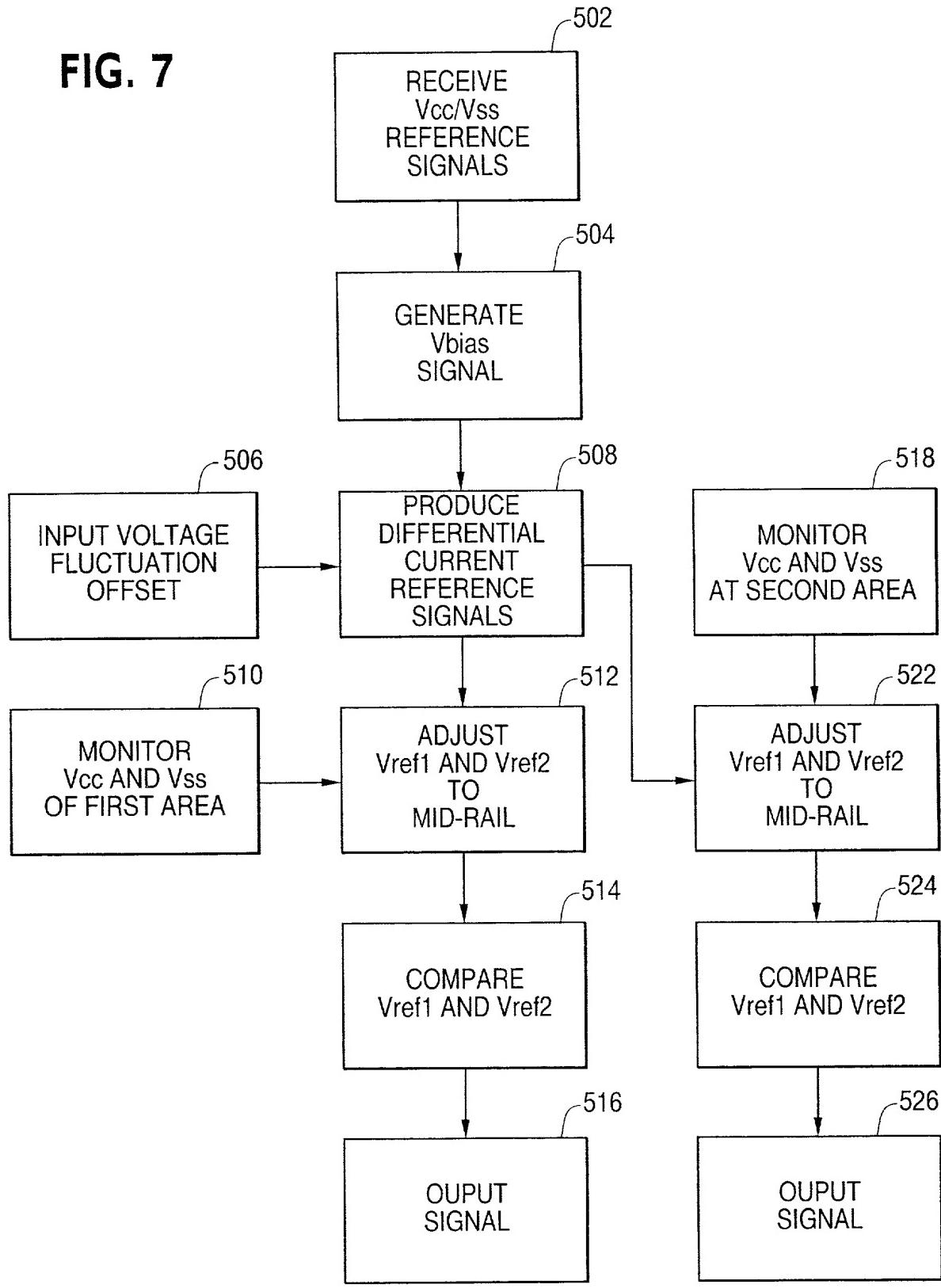


FIG. 8

